

Fig. 1

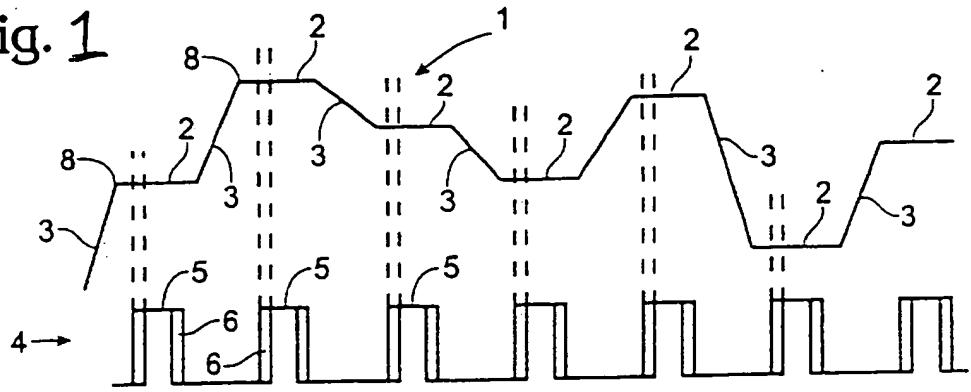


Fig. 2

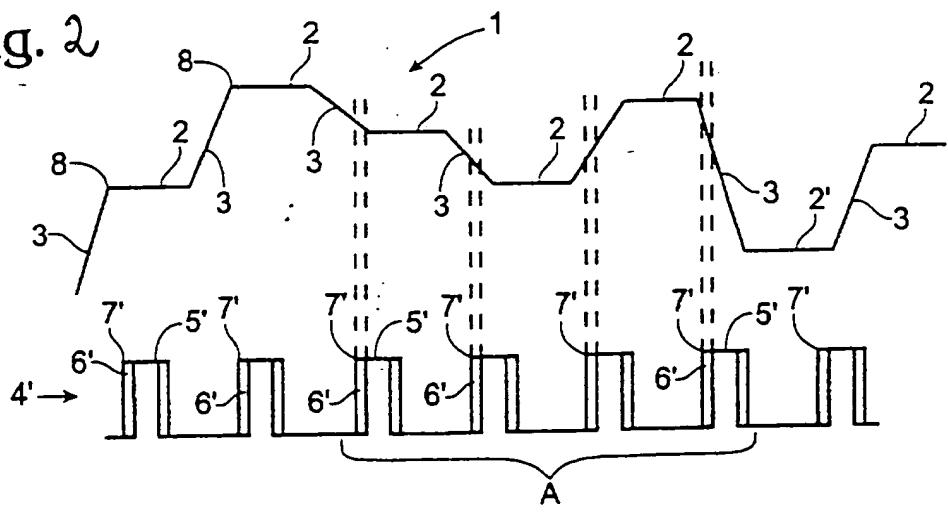


Fig. 3

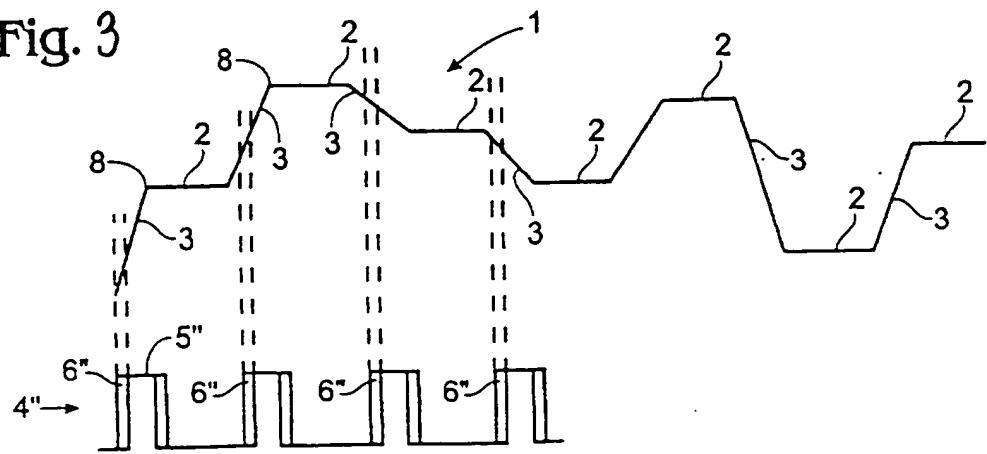


Fig. 4

9

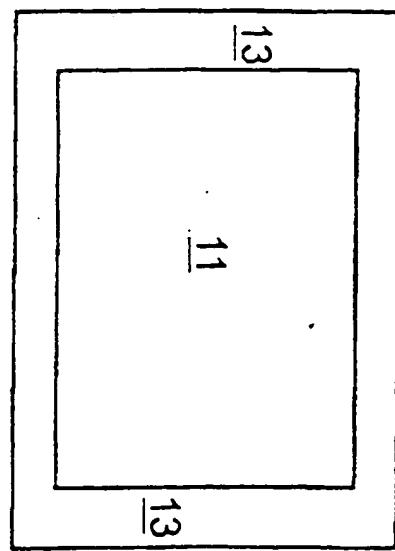


Fig. 5

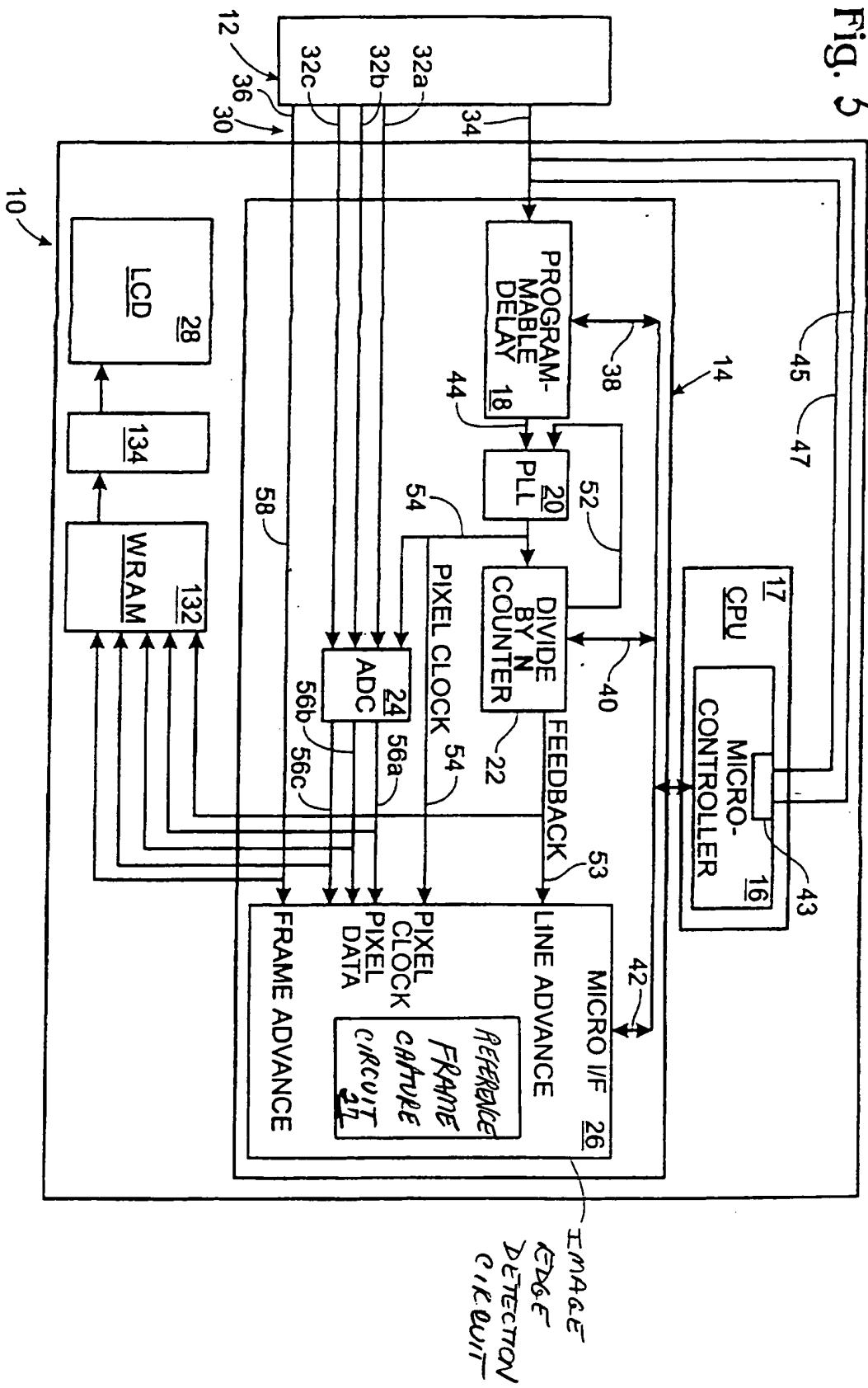


Fig. 6

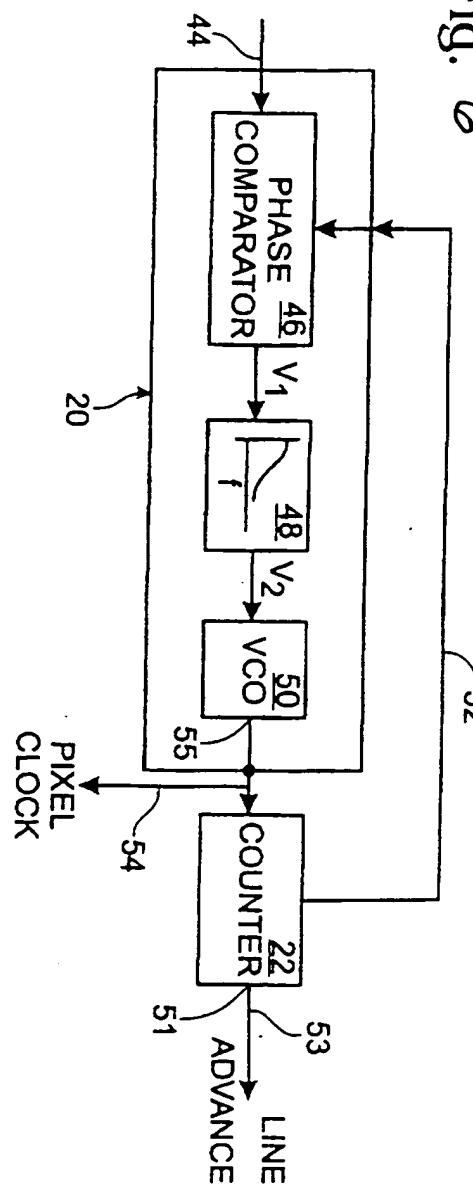
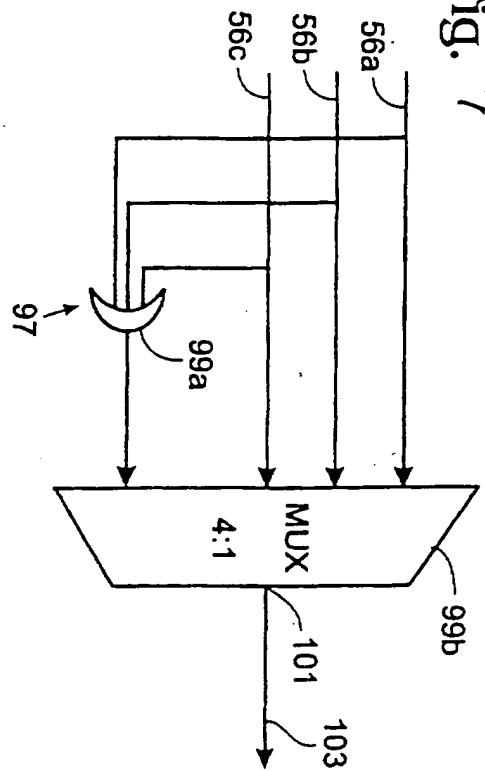
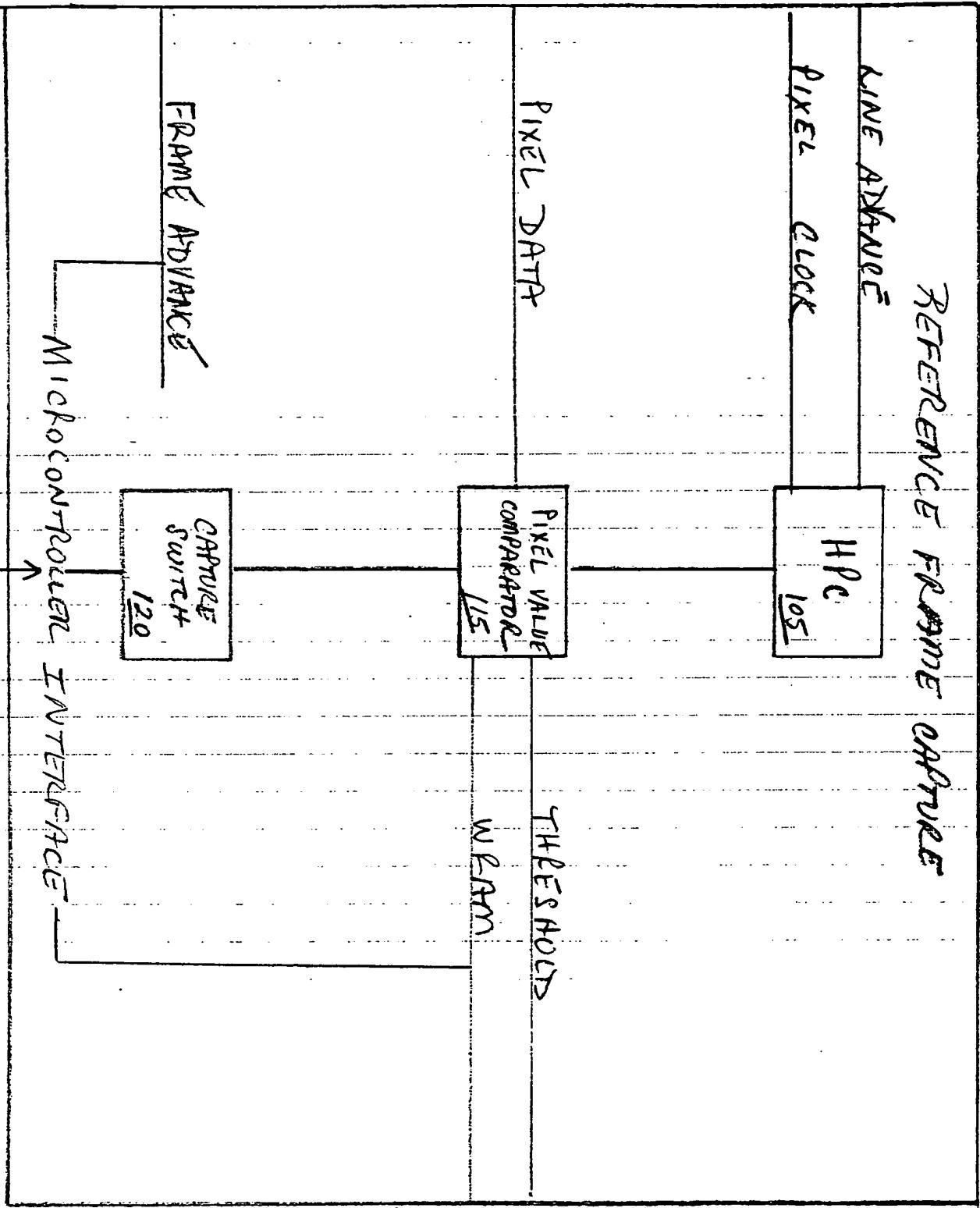


Fig. 7

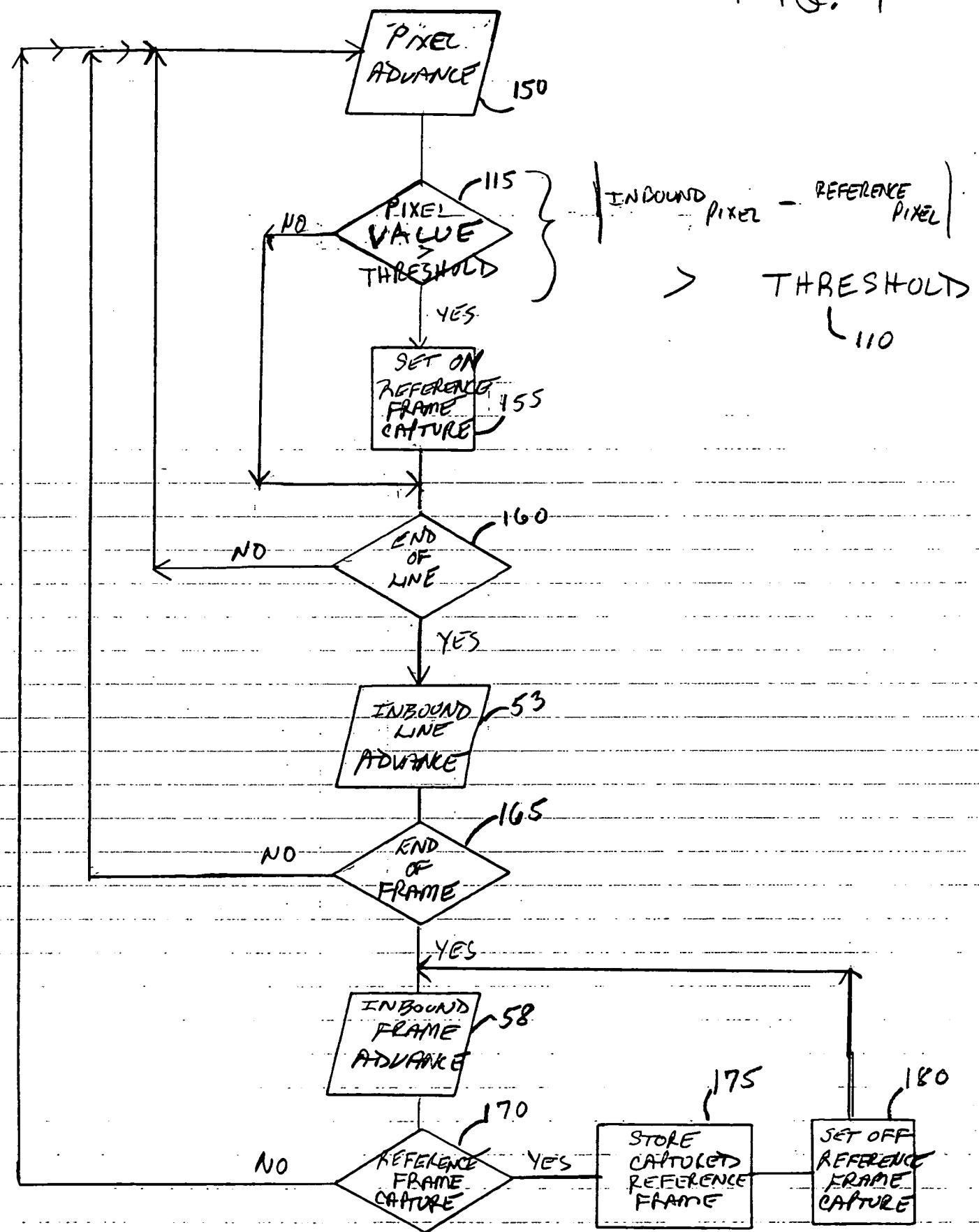


REFERENCE FRAME CAPTURE



110
132

FIG. 9



58		HPC		Horizontal Position Counter		Pixel Value	Action
Frame		Line		Comparison			STORE 1175
1	LINE 1	0	0	FF	0	0	0
		2	2	FC	1	0	-2
		3	0	FF	0	1	-2
		4	3	FC	0	2	-2
		5	3	FC	2	3	-2
2	LINE 2	0	0	FC	0	0	0
		2	1	FB	2	0	FC
		3	0	FF	0	1	FB
		4	3	FC	0	2	FB
		5	3	FB	2	3	FB
3232	LINE 1	3	3	FC	0	0	FC
		2	1	FD	2	0	0
		3	0	2	FC	0	0
		4	3	FC	0	1	FF
		5	3	4	FB	2	3
3233	LINE 1	5	5	FF	2	3	FF

FIG. 10

Reference Frame

No Change

No Change

SET 0/1
REFERENCE 115
FRAME CAPTURE

STORE 1175
REFERENCE
FRAME

—

No Change

No Change

SET 0/1
REFERENCE 115
FRAME CAPTURE

STORE 1175
REFERENCE
FRAME

—

No Change

No Change

SET 0/1
REFERENCE 115
FRAME CAPTURE

STORE 1175
REFERENCE
FRAME

—

F16. 11

INBOUND PIXEL
201

R	245
G	200
B	87

203 - R | 245
204 - G | 200
205 - B | 87

245 |
200 |
- |

241 |
204 |

= | = |

4 |

→ FALSE

4 → FALSE

13 → TRUE

> 5

REFERENCE 202
S T O R E D P I X E L

R	241
G	204
B	200